## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE REPLY TO OFFICE ACTION DATED 04/11/2008 Atty. Docket No. **SUZU1720** Applicant Takeshi Ishigaki Application Number Date Filed 10/586,951 07/25/2006 Title Semiconductor Integrated Circuit Device and Method of Outputting Signals on Semiconductor **Integrated Circuit** Group Art Unit Examiner 2816 Rojas, Daniel E. Confirmation Number: 3389

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

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In response to the Official Action mailed April 11, 2008, Applicant respectfully requests the Examiner reconsider the rejections of the Claims in view of this reply.